

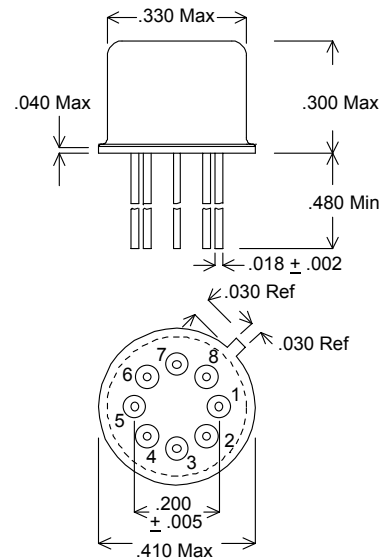


XE20-200A SERIES (HC/ACMOS), 5.0 VDC STANDARD SPECIFICATIONS

Frequency Range 400 KHz to 100 MHz
 Frequency Accuracy @ +25 °C $\pm 0.0015\%$ (± 15 PPM)
 Frequency Stability Vs. Temperature See Options Below
 Operating Temperature Range See Options Below
 Input Voltage + 5 VDC $\pm 10\%$

Input Current @ +5.0 VDC (No Load)

400 KHz to 5.0 MHz	10 mA Max.
5.1 MHz to 20.0 MHz	20 mA Max.
20.1 MHz to 40.0 MHz	30 mA Max.
40.1 MHz to 60.0 MHz	40 mA Max.
60.1 MHz to 100.0 MHz	60 mA Max.



All Dimensions are in inches.

Output HC/ACMOS
 Load 10 K Ω in parallel with 15 pf
 Symmetry 60/40% @ 50% Output Level
 Rise & Fall Times (10% to 90% Level)

≤ 20 MHz	10 nS Max.
> 20 MHz	5 nS Max.

Enable/Disable See Options Below

Start-Up Time 5 mS Max.

Frequency Stability Vs. Voltage $\pm 0.0004\%$ (± 4 PPM) Max.
 (for 10% change in Voltage)

Aging @ +25 °C ± 5 PPM Max. first year, ± 2 PPM Max./ Yr. thereafter

Package, Seal & Lead Finish Conforms to the Requirements of MIL-PRF-55310

Pin #	Function
3	E/D (Optional)
4	GND/CASE
5	OUTPUT
8	B+
All Others	N/C

Contact Xsis Engineering for special requirements such as, **Output Symmetry, Start-up Time, Frequency Accuracy, Complementary Outputs, Multiple Outputs, etc.**

ORDERING INFORMATION (Select from options below) :

XE20 - 2 [] [] **A** [] - [] - **FREQUENCY**

Frequency Stability

- 1 = $\pm 0.1\%$
- 2 = $\pm 0.05\%$
- 3 = $\pm 0.01\%$
- 4 = $\pm 0.005\%$
- 5 = $\pm 0.002\%$ *

*Option 5 not available for - 55 °C to +125 °C
 & - 55 °C to +105 °C

Operating Temperature Range

- 1 = 0 °C to + 70 °C
- 2 = - 30 °C to + 85 °C
- 3 = - 55 °C to +125 °C
- 4 = - 55 °C to +105 °C
- 5 = - 40 °C to + 95 °C

Add Suffix " 883B " for Mil-Screened Option

Add Suffix " G " for Enable/Disable Option **

** Enable/Disable Input: A "low" level at the input disables the output to a HI-Z state. Enable/disable input has internal pull-up.

EXAMPLE: XE20 -243A - 883B - 24.000 MHz = HCMOS Output, $\pm 0.005\%$ over -55 °C to +125 °C, Mil - Screened , and Output Frequency of 24.000 MHz