



XE101-200A SERIES (HC/ACMOS) LCC PACKAGE WITH 'J' LEADS STANDARD SPECIFICATIONS

FREQUENCY RANGE	400 KHz to 90 MHz
FREQUENCY ACCURACY @ +25 °C	± 0.0015% (± 15 PPM)
FREQUENCY STABILITY Vs. TEMPERATURE	See Options Below
OPERATING TEMPERATURE RANGE	See Options Below
INPUT VOLTAGE	+ 5 VDC ± 10%

INPUT CURRENT @ +5.0 VDC	
400 KHz to 5.0 MHz	10 mA Max.
5.1 MHz to 20.0 MHz	20 mA Max.
20.1 MHz to 40.0 MHz	30 mA Max.
40.1 MHz to 60.0 MHz	40 mA Max.
60.1 MHz to 90.0 MHz	60 mA Max.

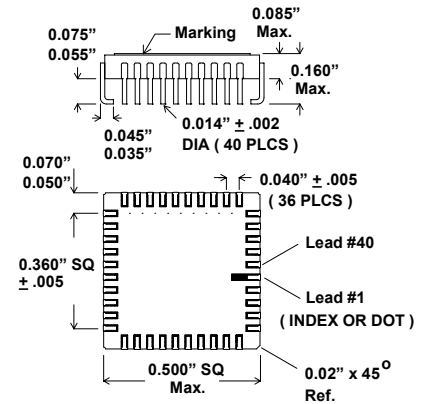
OUTPUT	HC/ACMOS
LOAD	10 KΩ in parallel with 15 pf
SYMMETRY	60/40% @ 50% Output Level
RISE & FALL TIMES (10% to 90% Level)	
≤ 20 MHz	10 nS Max.
> 20 MHz	5 nS Max.

START-UP TIME	
< 10 MHz	15 mS Max.
≥ 10 MHz	10 mS Max.

FREQUENCY STABILITY Vs. VOLTAGE (for 10% change in Voltage)	± 0.0005% (± 5 PPM) Max.
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AGING @ +25 °C	± 0.0005% (± 5 PPM) / year Max.
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PACKAGE, SEAL & LEAD FINISH	Conforms with the Requirements of MIL-PRF-55310
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Lead #	Function
4	B+
10	B+
31	GND
32	Enable (Option)
37	GND
39	OUTPUT
All others	NC

Contact Xsis Engineering for special requirements such as, Output Symmetry, Start-up Time, Frequency Accuracy, Complementary Outputs, Multiple Outputs, etc.

ORDERING INFORMATION (Select from options below) :

XE101 - **2** **A** - - **FREQUENCY**

Frequency Stability

- 1 = ± 0.1%
- 2 = ± 0.05%
- 3 = ± 0.01%
- 4 = ± 0.005%
- 5 = ± 0.002% *

* Option 5 not available for - 55 °C to +125 °C

Operating Temperature Range

- 1 = 0 °C to + 70 °C
- 2 = - 30 °C to + 85 °C
- 3 = - 55 °C to +125 °C

Add Suffix " 883B " for Mil-Screened Option

Add Suffix " G " for Enable/Disable Option **

** Enable/Disable Input is on Pad 32. A "low" level at the input disables the output to a HI-Z state. Enable/disable input has internal pull-up.

EXAMPLE: XE101-243A - 883B - 24.000 MHz = XE10 Package with ' J ' Leads, HCMOS, ± 0.005% over -55 °C to +125 °C, Mil-Screened , and 24.000 MHz